

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2226344	IC\$1 (integrat\$3 with circuit\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:09
L2	22002	microprocessor with chip\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:18
L3	400	1 & (power with rout\$3 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:19
L4	63	2 & (power with rout\$3 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:20
L5	10333480	3 "7" memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:20
L6	285	3 & memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:20
L7	63	4 & memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:29
L8	31	7 & (rout\$3 near3 logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:32

L9	9	7 & ((rout\$3 near3 logic) with (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:33
L10	2	9 & automatic\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:33
L11	0	7 & ((rout\$3 near3 logic) with automatic\$4 with (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:34
L12	1	6 & ((rout\$3 near3 logic) with automatic\$4 with (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:43
L13	0	12 & (text\$3 graphical\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:41
L14	1	6 & ((rout\$3 near3 logic) same automatic\$4 same (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:42
L15	0	8 & ((rout\$3 near3 logic) same automatic\$4 same (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:42
L16	0	7 & ((rout\$3 near3 logic) same automatic\$4 same (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:42

L17	12	1 & ((rout\$3 near3 logic) with automatic\$4 with (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:44
L18	0	2 & ((rout\$3 near3 logic) with automatic\$4 with (contact\$3 connect\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:44
L19	12	17 & (contact\$3 connect\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:45
L20	3	19 & (memory with stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:55
L21	3	19 & (transistor\$1 with cell\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 16:02
L22	3	21 & (memory stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 15:55
L23	0	20 & (transistor\$1 with cell\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 16:02
L24	0	20 & (transistor\$1 SAME cell\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 16:02

L25	0	20 & (transistor\$1 & cell\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 16:03
L26	2	22 & metal\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 16:20
L27	9	1 & (automatic\$4 with rout\$3 with ground\$3 with (IC integrat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:05
L28	5	27 & (automatic\$5 with power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 16:57
L29	23	1 & (automatic\$4 with rout\$3 with power with (IC integrat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:23
L30	338	1 & (automatic\$4 with rout\$3 with power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:05
L31	0	(genichi adj tanaka).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:24
L32	60	(Genichi near3 tanaka).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:25

L33	19	32 & (automatic\$4 near5 rout\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:38
L34	4	32 & (power with (automatic\$4 near5 rout\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 18:16
L35	2	32 & (method\$1 with (design\$3 & creat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:41
L36	181	1 & (power with (automatic\$4 near5 rout\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 17:43
L37	1	34 & analy\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/28 18:16